

INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO. 147-98 4147-98	SERIAL NO. 104238 10/5/8, 238
	APPLICANT HELLBERG, R.	
(Use several sheets if necessary)	FILING DATE December 16, 2004	TC/A.U. To be assigned 2817

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
H-c.	5,923,215	7/1999	Hans	—	—	
H-c.	6,028,477	2/2000	Gentzler	—	—	
H-c.	5,808,511	9/1998	Kobayashi	—	—	
H-c.	5,757,229	5/1998	Mitzlaff	—	—	

FOREIGN PATENT DOCUMENTS

FOREIGN MEDIUM DOCUMENTS							TRANSLATION	
DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO		

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

H- c.	S.C. Cripps; "Conventional High-Efficiency Amplifier Modes;" <i>RF Power Amplifiers for Wireless Communications</i> ; Artech House; Boston; 1999; pages 45-60.
H- c.	Lawrence J. Kushner; "Output Performance of Idealized Microwave Power Amplifiers;" <i>Microwave Journal</i> ; October 1989; pages 103-116.
H- c.	K.-J. Youn, et al.; "Low Dissipation Power and High Linearity PCS Power Amplifier with Adaptive Gate Bias Control Circuit;" <i>Electronics Letters</i> ; Aug. 15, 1996; Vol. 32, No. 17; pages 1533-1535.
H- c.	T. Iwai et al.; "42% High-Efficiency Two-Stage HBT Power-Amplifier MMIC for W-CDMA Cellular Phone System;" <i>IEEE Transactions MTT</i> ; Vol. 48, No. 12; Dec. 2000; pages 2567-2572.
H- c.	A. Saleh and D. Cox; "Improving the Power-Added Efficiency of FET Amplifiers Operating with Varying-Envelope Signals;" <i>IEEE Trans. MTT</i> ; Vol. 31, No. 1; Jan. 1983; pages 51-56.
H- c.	T. H. Miers and V. A. Hirsch; "A Thorough Investigation of Dynamic Bias on Linear GaAs FET Power Amplifier Performance;" 1992 <i>IEEE MTT-S Digest</i> ; pages 537-540.
H- c.	D. R. Conn and R. H. Hemmers; "Increased Efficiency in QAM Power Amplifiers;" 1998 <i>IEEE MTT-S Digest</i> ; pages 1647-1650.
H- c.	I. K. Stubbs; "A Dynamic Efficient Bias Scheme Improves SSPA Performance in Aeronautical Satellite Communication Systems;" <i>IEEE Colloquium on 'Evolving Technologies for Small Earth Station Hardware,' Digest No. 1995/037</i> ; IEEE, London, UK; 44 pages 5/1-5/8.
H- c.	Jean-Serge Cardinal and Fadhel M. Ghannouchi; "A New Adaptive Double Envelope Feedback (ADEF) Linearizer for Solid State Power Amplifiers;" <i>IEEE Trans. MTT</i> ; Vol. 43, No. 7; July 1995; pp. 1508-1515.

***Examiner**

Henry choe

Date Considered

3/20/07

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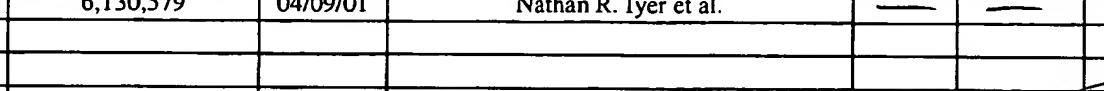
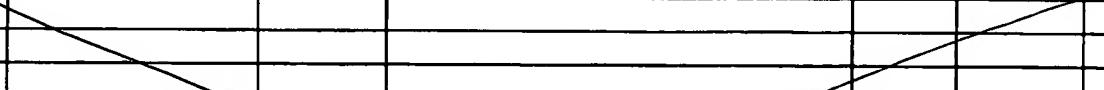
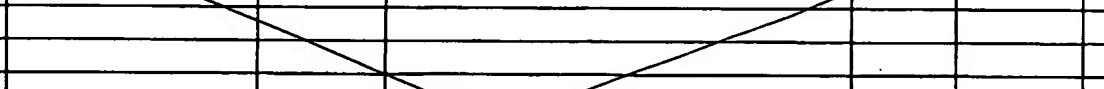
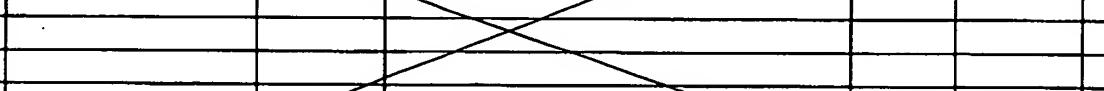
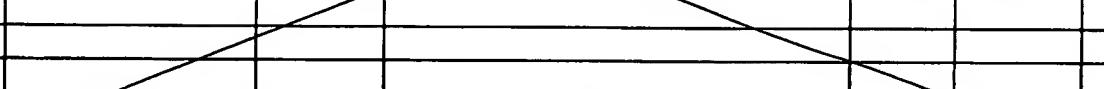
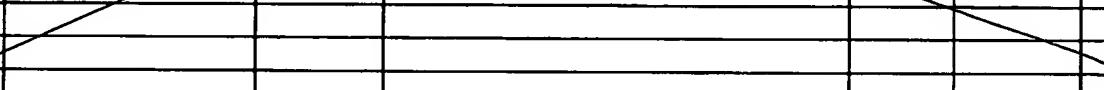
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December 15, 2004

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U.S. PATENT DOCUMENTS

U.S. PATENT DOCUMENTS					FILING DATE		
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	IF APPROPRIATE	
H. C.	6,130,579	04/09/01	Nathan R. Iyer et al.	—	—		
							
							
							
							
							
							

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

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